





Synchrotron Light Source in Cerdanyola (Barcelona, Spain)



July 27th, 2006: Oficial start of the building work

ALBA RF System 2/21









ALBA RF System 4/21











RF Lab

Up to 80 kW

In Spring 2007 installed provisionally in University Campus

In Autum 2008 will be transllated to the new bulding





Storage ring cavities : DAMPY

- Nose cones cavity with ferrite loaded dampers.
- The contract was signed in April 2006 with ACCEL for 7 identical pieces including girders, dampers, input windows and tuners.
- The CELLS prototype delivery is scheduled for January 2007.





Dampy : modifications

- 2 spacers have been added to the girder to support the ridged waveguides.
- The wedges on which ferrite tiles are brazed can be taken apart without removing the whole damper.
- Vacuum gauge and a sapphire window at the end of one waveguide







Booster cavity

- 5 cells cavity operating in π mode very similar to PETRA cavities.
- The contract was signed in April with ACCEL.
- The design report was approved in September 2007.
- Delivery is scheduled for January 2007.

RF Transmitter awarded to Thomson :

IOT Cabinet



- Delivery and installation of 14 transmitters (80 kW cw):
 - IOT
 - HVPS
 - SSA
 - Aux. PS
 - Control system
- Kick-off meeting by next weeks with Thomson
- 1st unit by May 2007 for the RF High Power Lab
- Following units from mid 2008

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WG Systems

- > Tendering done in 3 batches:
 - BATCH 1 : Standard Waveguide lines (WR1800)
 - Straights, Bends, bellows, bidirectional couplers, transitions
 - BATCH 2 : Circulators
 - BATCH 3 : Dry Loads
- ➤ 3 Companies in the tendering:
 - AFT
 - Ferrite Inc.
 - MEGA Industries
- Decision to be validated



2. Ferrite Inc.

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3. AFT





Low Level RF Analogue Prototype

I/Q Modulation / Demodulation



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LLRF Analog prototype



- The ALLRF prototype has been successfully tested with a mock-up cavity.
- The measured group delay of the low-level electronics is 500 ns.
- The noise level is better than what we had specified (i.e. 1% and 1°).
- A LLRF bandwidth of 1 MHz and a dynamic range of >23 dB have been achieved.







Low Level RF Digital Prototype



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Analog Front Ends and Timing System



-Up/Down conversion from RF to IF: Mixers and low pass filters from Mini-Circuits

-Digital clocks for ADCs and DACs: Clock synchronizer board from Texas Instruments

-Reference Signal (RF + 12.5MHz) for down and up conversion.





IQ Control Loops: Amplitude





Amplitude = $\sqrt{I^2 + Q^2}$ Total Amplitude Stability = $\frac{\pm 1.2mV}{134.55 \cdot \sqrt{2}} = 0.63\%$

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IQ Control Loops: Delay and Phase





Group Delay: Around 2.5 - 3µs



Phase Stability:
±0.2°at top of the ramp
±3°at bottom of the ramp



Analog/Digital LLRF comparison -Performance

	ALLRF	DLLRF
Group delay	500 ns	3 µs
Bandwidth	1 MHz	-
Ripple removal up to	30 kHz	2 kHz
Phase stability	±0.5 °	±0.2 °
Amplitude stability	±0.5 %	±0.75 %
Dynamic range	23 dB	20 dB

The stability, speed and dynamic range of the DLLRF is supposed to improve after analogue front end improvements.



Summary

- 1) SR Dampy Cavity: 1st unit in January 2007
- 2) Booster Cavity: 1st unit in January 2007
- 3) RF Transmitter: 1st unit in May 2007
- 4) Waveguide:
- 5) LLRF:

- Awarding in the following weeks
- Prototypes ready Tests at ELETTRA with cavity next week

6) **RF Lab**:

Foreseen for May 2007



The RF Group

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